IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

ın re F	atent .	Applica	ation of:			
Keisul	ke MUI	RAYA,	et al.			
Applic	ation N	lo.:		Group Art Unit:		
Filed:	Febru	ary 19	, 2004	Examiner:		
For:	SEM	CONE	DUCTOR INTEGRATED CIRCU AVING FUNCTION FOR MEAS	E FLUCTUATION WAVEFORM IN IT, AND SEMICONDUCTOR INTEGRATED URING VOLTAGE FLUCTUATION		
			INFORMATION DISCLO	SURE STATEMENT		
PO Bo	ox 1450)	Patents 313-1450			
Sir:						
the su	ed cer	tain int J.S. pa	formation which the Examiner m	rovisions of 37 CFR § 1.56, there is hereby ay consider material to the examination of that the Examiner make this information of f the subject application.		
	1.	Enc	losures accompanying this Infor	mation Disclosure Statement are:		
	1b 1c 1d		An English language copy of sapplication or a PCT Internation English language Abstract attapublication. Explanations of Relevancy of I	earch report(s) from a counterpart foreign nal Search Report. Inched to each non-English language References (ATTACHMENT 1(e), hereto) for In of each non-English publication.		
	2.			a concise explanation of what is presently each non-English language publication is		
	2a	. 🗆	enclosed "English-language ve indicates the degree of relevant 609, Minimum Requirements f	s 2a, 2b, 2c and/or 2d) sh language publications were cited on the ersion of the search report or action which nee found by the foreign office". (See MPEP or an Information Disclosure Statement, Part Relevance, pp. 600-100 to 600-101, Rev. 1,		

2b.		set forth in the application.
2c.	$\overline{\boxtimes}$	satisfied because an English language Abstract is attached to each
		non-English language publication.
2d.		enclosed as Attachment 1(e), hereto.
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3. No admission is made that the information cited in this Statement is, or is considered to be, material to patentability nor a representation that a search has been made (other than search report(s) from a counterpart foreign application or a PCT International Search Report, if submitted herewith). 37 CFR §§ 1.97(g) and (h).

Respectfully submitted,

STAAS & HALSEY LLP

Dated: February 19, 2004

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U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

1075.1249

APPLICATION NO.

LIST OF REFERENCES CITED BY APPLICANT

(Use several sheets if necessary)

FIRST NAMED INVENTOR

Keisuke MURAYA, et al.

FILING DATE

February 19, 2004

GROUP ART UNIT

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	AA						
	АВ						
	AC						
	AD						
	AE		2				
	AF						

FOREIGN PATENT DOCUMENTS

	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSI YES	ATION NO
AG	JP 2001- 59855	3/6/01	Japan			Abst	
АН							
Al							
AJ							
AK							
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OTHER REFERENCES (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

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DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.